Call for Participation
The Second IEEE Asian Solid-State Circuits Conference (A-SSCC)
Nov. 13 - Nov. 15, 2006
Hyatt Regency Hangzhou, China

Challenges for THE e-life

The IEEE SSCS (Solid-State Circuits Society) fully supported Asian Solid-State Circuits Conference (A-SSCC) 2006 is an international forum for advances in solid-state circuits design held in Asia. Other than the conventional conference programs such as paper and plenary sessions, A-SSCC features a practical industry program. In the very second year under its name, the A-SSCC'06 received more than 326 papers. Technical program committee members are a balance of the industry and the academia and as a result, the technical program should interest both industry and academia.

Technical Paper Focus

"A Block Scaling FFT/IFFT Processor for WiMAX Applications"
"A 1.5MS/s, 6-bit ADC with 0.5V supply"
"A 19.7 MHz, 5th Order Active-RC Chebyshev LPF for IEEE802.11n with Automatic Quality Factor Tuning Scheme"
"Stacked-Chip Implementation of On-Chip Buck Converter for Power-Aware Distributed Power Supply Systems"
"Adaptive Self Refresh Scheme for Battery Operated High-Density Mobile DRAM Applications"
"94GHz SiGe down conversion mixer"
"A 103 GOPS Signal Processing Platform Chip for Software Defined Radio"
"A 1.4Gbps/ch LVDS Receiver for Flat Panel Displays"

Four Plenary Talks

Richard Ru-Gin Chang, CEO of SMIC, China
“Seminconductor Advanced Technology Development and Challenges in China”
Soo-Young Oh, VP of ETRI, Korea
“Component Technologies needed by the Ubiquitous IT Society”
Tohru Furuyama, GM of Toshiba, Japan
“Deep Sub-100 nm Design Challenges”
Ming-Kai Tsai, CEO of MediaTek, Taiwan
“From PC Multimedia Chipsets to Wireless and Digital Consumer SoC: Evolution and Challenges”

Two Evening Panels

“Future Digital Link”
Moderator: C. K. Ken Yang (UCLA, USA)
Panelist: Munee Fukaishi (NEC, Japan), Jri Lee (National Taiwan Univ., Taiwan), Sung Min Park (Ewha Womans Univ., Korea), Hirotaka Tamura (Fujitsu Lab., Japan)
“Software Defined Radio; How to realize soft and flexible RF and baseband circuits?”
Moderator: A. Matsuzawa (Tokyo Inst. Of Tech., Japan)
Panelist: Mototsugu Hamada (Toshiba, Japan), Howard C. Luong (Hong Kong Univ., China), Kathleen (Philips, Netherlands), Hyun-Kyu Yu (ETRI, Korea)

Industrial Program

This program is inspired by Nicky Lu, a fellow of the IEEE. In the industry program, papers on cutting-edge IC products are presented with application, designed measurement information. Two parallel sessions with total 8 papers from Intel (USA), Industrial Technology Research Institute (Taiwan), Intellectual Property Library Company (Taiwan), Sun Microsystems (USA), THine Electronics Inc. (Japan), eMemory Technology Inc. (Taiwan), Hynix Semiconductor Inc. (Korea), and MediaTek Inc. (Taiwan).

Student Design Contest

10 outstanding chip designs covering from 802.11b single CMOS chip to low power Body Sensor Network Controller chip out of the accepted student papers will make real demonstrations on the conference site with authors’ explanation. The best student design will be selected among those 10 demonstrations reflecting the high research standards of Asian academies.

Education Sessions

4 in-depth tutorials
(1) Embedded SRAM Design (Hiroyuki Yamauchi, Fukuoka Institute of Technology)
(2) PLL Design (Howard C. Luong, Hong Kong University)
(3) Pipeline ADC Design (Yun Chiu, University of Illinois)
(4) Sigma-delta ADC Design (Kathleen Philips, Philips Research Laboratories)

Technical Tour

A FREE technical tour is scheduled in the morning of November 16 for conference attendants who are interested in Hangzhou's high-tech industrial environment and related research resources.