

9th Japan-Taiwan Microelectronics International Workshop -- Innovation of Sensing Technologies --		
Date & Venue      October 28 and 29, 2009 at Takeda Hall, The University of Tokyo Co-sponsored by:   VLSI Design and Education Center(VDEC), The University of Tokyo Interchange Association, Japan (IAJ) National Science Council, Taiwan (NSC) Taipei Economic & Cultural Representative Office in Japan Cooperation with:   IEEE SSCS Japan Chapter, IEEE SSCS Kansai Chapter		
Oct. 28		
Opening		
9:30- 9:45		Welcome remark <i>Prof. Kunihoro Asada (浅田 邦博), The University of Tokyo</i>
		Welcome remark <i>Vice President Jing-Yang Jou (周景揚), National Chiao Tung University</i>
Session1		
9:45-10:15	1-1	Si-Based High-Sensitivity CMOS Sensing Chips and SPR waveguides for Biomedical Applications <i>Prof. Yang-Tung Huang (黃遠東), National Chiao Tung University</i>
10:15-10:45	1-2	Carbon Nanotube Photonics <i>Prof. Shinji Yamashita (山下 真司), The University of Tokyo</i>
10:45-11:00		break
11:00-11:30	1-3	The Novel Laser Interference Lithography System for the Production of Large Area Nano Imprinting Molds-Idea, Fabrication and Applications <i>Prof. Chien-Chung Fu (傅建中), National Tsing Hua University</i>
11:30-12:00	1-4	CIC CMOS MEMS/BioMEMS Design Platform for Sensor IP and System <i>Dr. Chin-Fong Chiu (邱進峰), National Applied Research Lab.</i>
		Lunch
Session2		
13:30-14:00	2-1	Design and Fabrication Techniques for Integrated MEMS <i>Prof. Hiroshi Toshiyoshi (年吉 洋), The University of Tokyo</i>
14:00-14:30	2-2	CMOS BioMEMS Protein Sensors and Drug Delivery SoCs <i>Prof. Shey-Shi Lu (呂學士), National Taiwan University</i>
14:30-15:00		break
15:00-15:30	2-3	Future millimeter-wave / microwave imaging technology and microelectronics <i>Prof. Akira Hirose (廣瀬 明), The University of Tokyo</i>
15:30-16:00	2-4	CMOS-based Wireless Sensing Nodes for Better Medical Services <i>Prof. Chen-Yi Lee (李鎮宜), National Chiao Tung University</i>
16:00-16:30	2-5	Stand-off Gas Sensing with A Highly- Sensitive Sub-Terahertz Receiver <i>Dr. Yuichi Kado (門 勇一), NTT</i>
16:30-17:00		break
Panel Discussion		
17:00-18:00		Subject: Are you ready for More-than-Moore? Moderator: <i>Prof. Toru Nakura (名倉 徹), The University of Tokyo</i> Panelist: <i>Prof. Kunihoro Asada (浅田 邦博), The University of Tokyo</i> <i>Prof. Hiroshi Toshiyoshi (年吉 洋), The University of Tokyo</i> <i>Prof. Akira Hirose (廣瀬 明), The University of Tokyo</i> <i>Prof. Jing-Yang Jou (周景揚), National Chiao Tung University</i> <i>Prof. Prof. Chen-Yi Lee (李鎮宜), National Chiao Tung University</i> <i>Prof. Prof. Wei Hwang (黃威), National Chiao Tung University</i>
Oct.29		
Session 3		
9:30-10:00	3-1	Printed organic transistors and ambient electronics <i>Prof. Takao Someya (染谷 隆夫), The University of Tokyo</i>
10:00-10:30	3-2	The Development of Flexible Sensors <i>Dr. Yi-Jen Chan (詹益仁), Industrial Technology Research Institute (ITRI)</i>
10:30-11:00		break
11:00-11:30	3-3	Life Log Technology <i>Prof. Kiyoharu Aizawa (相澤 清晴), The University of Tokyo</i>
11:30-12:00	3-4	Creatinine and Neopterin Biosensors for Diagnosis of Renal Function and Cellular Immunity <i>Prof. Bin-Da Liu (劉濱達), National Cheng Kung University</i>
		Lunch
Session 4		
13:30-14:00	4-1	Development of Cryogenics Integrated Circuits for Superconducting Terahertz Camera <i>Prof. Hiroshi Matsuo (松尾 宏), National Astronomical Observatory of Japan (NAOJ)</i>
14:00-14:30	4-2	Ultra-Low Voltage Design Challenges for Micro-Watt Sensor Systems and Applications <i>Prof. Wei Hwang (黃威), National Chiao Tung University</i>
14:30-15:00	4-3	Development of cryogenic readout electronics for astronomical far-infrared cameras <i>Dr. Hirohisa Nagata (永田 洋久), Japan Aerospace Exploration Agency (JAXA)</i>
15:00-15:30		break
15:30-16:00	4-4	Circuit and System Implementation of An Implantable Biomedical Device for Micro-Stimulation and Neural Sensing <i>Prof. Chua-Chin Wang (王朝欽), National Sun Yat-Sen University</i>
16:00-16:30	4-5	The actual situation of the image sensor performance innovation <i>Mr. Tadakuni Narabu (奈良部 忠邦), SONY</i>
16:30-17:00		break
17:00-17:30	4-6	Data Converters in Nanoscale CMOS Technologies <i>Prof. Jieh-Tsong Wu (吳介琮), National Chiao Tung University,</i>
17:30-18:00	4-7	Novel Electrical Failure Analysis Tools for LSI chips: non-bias and non-signal-application <i>Dr. Kiyoshi Nikawa (二川 清), NEC Electronics</i>
Closing		
18:00-18:15		Closing remark <i>Prof. Kunihoro Asada (浅田 邦博), The University of Tokyo</i>
		Closing remark <i>Vice President Jing-Yang Jou (周景揚), National Chiao Tung University</i>
		Banquet